

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE


[Membership](#) [Publications/Services](#) [Standards](#) [Conferences](#) [Careers/Jobs](#)
IEEE Xplore®
 RELEASE 1.8

 Welcome
 United States Patent and Trademark Office


» Se.

[Help](#) [FAQ](#) [Terms](#) [IEEE Peer Review](#)
[Quick Links](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Print Format

 Your search matched **8** of **1105713** documents.

 A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering new one in the text box.

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Variable gain differential current feedback amplifier

Koudar, I.;

Custom Integrated Circuits Conference, 2004. Proceedings of the IEEE 2004 , Oct. 2004

Pages:659 - 662

[\[Abstract\]](#) [\[PDF Full-Text \(496 KB\)\]](#) IEEE CNF

2 A CMOS differential buffer amplifier with accurate gain and clipping control

Chang, Z.Y.; Haspeslagh, D.;

Solid-State Circuits, IEEE Journal of , Volume: 30 , Issue: 7 , July 1995

Pages:731 - 735

[\[Abstract\]](#) [\[PDF Full-Text \(424 KB\)\]](#) IEEE JNL

3 Ultra low noise 2.5 Gbit/s 3.3V transimpedance amplifier with autor gain control

Gasmi, A.; Wroblewski, B.; Leblanc, R.; Smith, D.; Rocchi, M.;

Gallium Arsenide Integrated Circuit (GaAs IC) Symposium, 2001. 23rd Annual Technical Digest , 21-24 Oct. 2001

Pages:61 - 64

[\[Abstract\]](#) [\[PDF Full-Text \(275 KB\)\]](#) IEEE CNF

4 A 20-dB CMOS IF amplifier with embedded single-to-differential input converter

Palmisano, G.; Pennisi, S.;

Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 2 , 30 May-2 June 1999

Pages:196 - 199 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(224 KB\)\]](#) [IEEE CNF](#)

5 Design of an adaptive interference reduction system for nerve-cuff electrode recording

Demosthenous, A.; Taylor, J.; Triantis, I.F.; Rieger, R.; Donaldson, N.;
Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuit Systems I: Fundamental Theory and Applications, IEEE Transactions on] , Vol 51 , Issue: 4 , April 2004
Pages:629 - 639

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) [IEEE JNL](#)

6 Bipolar monolithic amplifiers for a gigabit optical repeater

Ohara, M.; Akazawa, Y.; Ishihara, N.; Konaka, S.;
Solid-State Circuits, IEEE Journal of , Volume: 19 , Issue: 4 , Aug 1984
Pages:491 - 497

[\[Abstract\]](#) [\[PDF Full-Text \(1184 KB\)\]](#) [IEEE JNL](#)

7 10-MHz 60-dB dynamic-range 6-dB variable gain amplifier

Leung, L.H.Y.; Buchwald, A.;
Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on , Volume: 1 , 9-12 June 1997
Pages:173 - 176 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(444 KB\)\]](#) [IEEE CNF](#)

8 A 110 MHz 70 dB CMOS variable gain amplifier

Mostafa, M.; Elwan, H.; Bellaour, A.; Kramer, B.; Embabi, S.H.K.;
Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Symposium on , Volume: 2 , 30 May-2 June 1999
Pages:628 - 631 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(256 KB\)\]](#) [IEEE CNF](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

EAS1 - (Untitled1:1)

File Edit View Options Help

Icons: [New] [Open] [Save] [Print] [Find] [Go] [Home] [Back] [Forward] [Stop] [Refresh] [Zoom In] [Zoom Out] [Full Screen] [Help]

Left Panel:

- Drafts
 - ISSA:
 - BRS:
 - BRS:
 - BRS:
 - BRS:
 - ISSA:
 - BRS:
- Pending
- Active
 - L3: (2668) differential ad) amplifier and gain ad) control and feedback
 - L4: (31) 3 and input ad) current ad) source
 - L5: (6) 4 and output ad) current ad) source**
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Top Bar:

Search: [Text Box] [Search] [Reset] [Cancel] [OK] [Cancel]

Buttons: [New] [Open] [Save] [Print] [Find] [Go] [Home] [Back] [Forward] [Stop] [Refresh] [Zoom In] [Zoom Out] [Full Screen] [Help]

Main Content Area:

4 and output ad) current ad) source

Bottom Panel:

DocID	DocumentID	Issue Date	Pages	Title	Current:UR	Current:Ref	Retrieval Class	Inventor	US	C	P	E	U	U	U	U	U	U
1	US 5410271 A	19950425	15	Non-linear feedback network providing a linear transfer function	330/110	330/260; 330/282		Arbel, Arish	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	US 4720856 A	19880119	9	Control circuit having a direct current control loop for controlling the gain of an attenuator	379/395.01	379/347; 379/390.01; 379/390.03		Pace, W. David et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	US 4514769 A	19850430	92	Method and apparatus for processing color video signals for recording and reproducing	386/40			Gallo, Luigi C.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	US 4477847 A	19841016	242	Television signal disc drive recorder	360/60	386/125; 386/94		Knight, Howard W. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	US 4349832 A	19820914	222	Digital data rate corrector and time base corrector	348/500	375/365; 386/20		Gallo, Luigi C.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	US 4270150 A	19810526	194	Video frame storage recording and reproducing apparatus	386/86	360/22; 360/63; 360/78.04		Diermann, Joachim P. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	US 4145704 A	19790320	123	Television subcarrier phase correction for color field sequencing	386/21			Gallo, Luigi C.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

Bottom Bar:

Ready [Status Bar]